

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Robert J. Proebsting et al.

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: INTEGRATED CIRCUIT DEVICES HAVING HIGH PRECISION DIGITAL DELAY
LINES THEREIN

Date: August 18, 2003

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

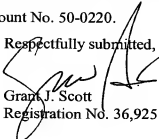
Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97(b) and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

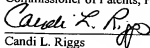

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CERTIFICATE OF MAILING UNDER 37 CFR § 1.10

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Mail Stop Patent Application, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Candi L. Riggs

FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				Attorney Docket Number 5646-81		Serial No. To Be Assigned	
				Applicants: Robert J. Proebsting			
				Filing Date: Concurrently Herewith		Group	
U. S. PATENTS & PATENT APPLICATION PUBLICATIONS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1	6,477,110 B2	11-5-02	Yoo et al.	365	233	
	2	6,411,142 B1	6-25-02	Abbasi et al.	327	156	
	3	6,400,202 B1	6-4-02	Fifield et al.	327	291	
	4	6,366,150 B1	4-2-02	Ishimi	327	276	
	5	6,359,489 B1	3-19-02	Huang	327	291	
	6	6,356,158 B1	3-12-02	Lesea	331	11	
	7	6,356,099 B1	3-12-02	Lee et al.	326	24	
	8	6,348,827 B1	2-19-02	Fifield et al.	327	278	
	9	6,285,229 B1	9-4-01	Chu et al.	327	277	
	10	6,285,172 B1	9-4-01	Torbey	323	237	
	11	6,275,899 B1	8-14-01	Savell et al.	711	118	
	12	6,255,969 B1	7-3-01	Crayford	341	100	
	13	6,239,892 B1	5-29-01	Davidson	359	135	
	14	6,134,180	10-17-00	Kim et al.	365	233	
	15	5,923,597	7-13-99	Tweed et al.	365	194	
	16	5,910,740	6-8-99	Underwood	327	149	
	17	5,847,616	12-8-98	Ng et al.	331	57	
	18	5,844,954	12-1-98	Casasanta et al.	375	373	
	19	5,828,257	10-27-98	Masleid	327	276	
	20	5,818,769	10-6-98	Tweed et al.	365	194	
	21	5,719,515	2-17-98	Danger	327	270	
	22	5,374,860	12-20-94	Llewellyn	327	276	
	23	5,317,219	5-31-94	Lupi et al.	307	603	

EXAMINER

DATE CONSIDERED

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Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	24	Ryan, Kevin, "DDR SDRAM Functionality and Controller Read Data Capture," Micron Technology, Inc., Volume 8, Issue 3, 1999, pp. 1-24					

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